

IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

SP

APPLICANTS: Steven G. Roskowski *et al.*  
APPLICATION NO.: REISSUE OF USPN 5,887,196  
FILING DATE: Herewith  
TITLE: SYSTEM FOR RECEIVING A CONTROL SIGNAL FROM A DEVICE FOR  
SELECTING ITS ASSOCIATED CLOCK SIGNAL FOR CONTROLLING THE  
TRANSFERRING OF INFORMATION VIA A BUFFER  
EXAMINER: UNKNOWN  
GROUP ART UNIT: UNKNOWN  
ATTY. DKT. NO.: 18602-05889

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PRELIMINARY AMENDMENT AND  
STATUS OF CLAIMS AND SUPPORT FOR CLAIM CHANGES  
UNDER 37 CFR § 1.173(c)

Sir:

Prior to examination of the subject reissue patent application, please add the following  
new claims:

21 [ 23. An apparatus for use in a computer system for transferring data from a first component to a second component, the first component clocked by a first clock signal, and the second component clocked by a second clock signal, the first clock signal being out of sync with the second clock signal, the apparatus comprising:  
a buffer coupled to the first component and the second component via a data path; and  
a multiplexer having inputs coupled to the first clock signal and the second clock signal,  
and an output coupled to the buffer, the multiplexer supplying the first clock signal  
to the buffer to clock data from the first component to the buffer via the data path in